



ENGINEERING CHANGE NOTE:



ECN #: 389

ASSEMBLY: BMW4011E

DESCRIPTION: PXY CPU CARD

New Assembly:

DATE: 28/09/00

Current Assy Rev: P

Current Schematic Rev: 5.9

New Assy Rev: Q

New Schematic Rev: 5.10

CMS BOMs Updated
 Excel BOMs Updated
 Subcontractor Records Updated
 Compatibility Maintained: Yes

IMPLEMENTATION INFORMATION:

- Safety Issue
- Bug Fix
- Reliability Reasons
- UL Compliance Issue
- EMC Compliance Issue
- Software Change
- New Feature
- Quality Issue
- Cosmetic Change
- Cost Reduction

REASON FOR CHANGE:

Allows EDO or FPM DRAM SIMMs to be used on PXY.

Note: This ECN is only mandatory if EDO DRAMs are installed. FPM DRAMs can still be used if this ECN is done. And all future production boards should have this ECN applied.

DETAILS OF CHANGE:

1. Connect U39/39 to U39/46.
2. Connect U40/39 to U40/46.
3. Connect U41/39 to U41/46.
4. Connect U42/39 to U42/46.
5. Place revision resistors (RMD8573 - RES 10K 0.125W 1% SMD0603) as follows;
 - R127 - 10k
 - R128 - Omit
 - R133 - Omit
 - R134 - 10k
 - R135 - 10k
 - R136 - Omit
 - R137 - Omit
 - R138 - 10k

Place a assembly revision label on the card, marked "Q", after the blank Rev 5 PCB P/N so it reads as " BMW4011E-Q".

HW or Project Engineer.....

R&D or Project Manager.....